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#### 1 [Coarse grain reconfigurable architecture \(embedded tutorial\)](#)

[Reiner Hartenstein](#)

January **ASP-DAC '01**: Proceedings of the 2001 conference on Asia South Pacific 2001 design automation

**Publisher:** ACM

Full text available: Pdf (167.05 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [cited by](#), [index terms](#)

**Bibliometrics:** Downloads (6 Weeks): 53, Downloads (12 Months): 259, Citation Count: 12

The paper gives a brief survey over a decade of R&D on coarse grain reconfigurable hardware and related compilation techniques and points out its significance to the emerging discipline of reconfigurable computing.

#### 2 [PACT HDL: a C compiler targeting ASICs and FPGAs with power and performance optimizations](#)

[Alex Jones](#), [Debabrata Bagchi](#), [Satrajit Pal](#), [Xiaoyong Tang](#), [Alok Choudhary](#), [Prith Banerjee](#)

October **CASES '02**: Proceedings of the 2002 international conference on 2002 Compilers, architecture, and synthesis for embedded systems

**Publisher:** ACM

Full text available: Pdf (340.93 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [cited by](#), [index terms](#)

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**Keywords:** ASIC, FPGA, FSM, HDL, IP, SoC, VHDL, Verilog, compiler, high-performance, levelization, low-power, pipelining, synthesis

3 A decade of reconfigurable computing: a visionary retrospective

R. Hartenstein

March 2001 **DATE '01**: Proceedings of the conference on Design, automation and test in Europe

**Publisher:** IEEE Press

Full text available:  Pdf (768.00 KB) Additional Information: [full citation](#), [references](#), [cited by](#), [index terms](#)


**Bibliometrics:** Downloads (6 Weeks): 57, Downloads (12 Months): 225, Citation Count: 53

4 Efficient Java RMI for parallel programming



November 2001 **Transactions on Programming Languages and Systems (TOPLAS)**, Volume 23 Issue 6

**Publisher:** ACM  [Request Permissions](#)

Full text available:  Pdf (352.63 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [cited by](#), [index terms](#), [review](#)

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Java offers interesting opportunities for parallel computing. In particular, Java Remote Method Invocation (RMI) provides a flexible kind of remote procedure call (RPC) that supports polymorphism. Sun's RMI implementation achieves this kind of flexibility ...

**Keywords:** Communication, performance, remote method invocation


5 Towards a multimedia formatting vocabulary



Jacco van Osserbruggen, Lynda Hardma, Joost Geurts, Lloyd Rutledge

May 2003 **WWW '03**: Proceedings of the 12th international conference on World Wide Web

**Publisher:** ACM

Full text available:  Pdf (189.77 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

**Bibliometrics:** Downloads (6 Weeks): 7, Downloads (12 Months): 44, Citation Count: 5

Time-based, media-centric Web presentations can be described declaratively in the XML world through the development of languages such as SMIL. It is difficult, however, to fully integrate them in a complete document transformation processing chain. In ...

**Keywords:** Cuypers, document transformation, formatting objects, hyper-media, multimedia


## 6 [Process migration](#)



[Dejan S. Milošević](#), [Fred Douglass](#), [Yves Paindaveine](#), [Richard Wheeler](#), [Songnian Zhou](#)

September 2000 **Computing Surveys (CSUR)** , Volume 32 Issue 3

**Publisher:** ACM  [Request Permissions](#)

Full text available:  Pdf (1.24 MB) Additional Information: [full citation](#), [abstract](#), [references](#), [cited by](#), [index terms](#), [review](#)

**Bibliometrics:** Downloads (6 Weeks): 82, Downloads (12 Months): 684, Citation Count: 38

Process migration is the act of transferring a process between two machines. It enables dynamic load distribution, fault resilience, eased system administration, and data access locality. Despite these goals and ongoing research efforts, migration has ...

**Keywords:** distributed operating systems, distributed systems, load distribution, process migration



## 7 [How to solve the current memory access and data transfer bottlenecks: at the processor architecture or at the compiler level](#)



[Francky Catthoor](#), [Nikil D. Dutt](#), [Christoforos E. Kozyrakis](#)

January 2000 **DATE '00: Proceedings of the conference on Design, automation and test in Europe**

**Publisher:** ACM

Full text available:  Publisher Site ,  Pdf (92.68 KB) Additional Information: [full citation](#), [references](#), [cited by](#), [index terms](#)

**Bibliometrics:** Downloads (6 Weeks): 5, Downloads (12 Months): 36, Citation Count: 5


## 8 [Eliminating synchronization overhead in automatically parallelized programs using dynamic feedback](#)



[Pedro C. Diniz](#), [Martin C. Rinard](#)

May 1999 **Transactions on Computer Systems (TOCS)** , Volume 17 Issue 2

**Publisher:** ACM  [Request Permissions](#)

Full text available:  Pdf (244.57 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [cited by](#), [index terms](#), [review](#)

**Bibliometrics:** Downloads (6 Weeks): 6, Downloads (12 Months): 38, Citation Count: 4

This article presents dynamic feedback, a technique that enables computations to adapt dynamically to different execution environments. A compiler that uses dynamic feedback produces several different versions of the same source code; each version uses ...

**Keywords:** parallel computing, parallelizing compilers

9 Model-driven development of Web applications: the AutoWeb system



Piero Fraternali, Paolo Paolini

October **Transactions on Information Systems (TOIS)** , Volume 18 Issue 4  
2000

**Publisher:** ACM [Request Permissions](#)

Full text available: Pdf (6.94 MB) Additional Information: [full citation](#), [abstract](#), [references](#), [cited by](#), [index terms](#)

**Bibliometrics:** Downloads (6 Weeks): 123, Downloads (12 Months): 434, Citation Count: 25

This paper describes a methodology for the development of WWW applications and a tool environment specifically tailored for the methodology. The methodology and the development environment are based upon models and techniques already used in the hypermedia, ...

**Keywords:** HTML, WWW, application, development, intranet, modeling

10 Towards second and third generation web-based multimedia



Jacco van Osserbrugger, Joost Geurts, Frank Cornelissen, Lynda Hardman, Lloyd Rutledge

April **WWW '01: Proceedings of the 10th international conference on World Wide Web 2001**

**Publisher:** ACM

Full text available: Pdf (563.48 KB) Additional Information: [full citation](#), [references](#), [cited by](#), [index terms](#)

**Bibliometrics:** Downloads (6 Weeks): 11, Downloads (12 Months): 75, Citation Count: 19

**Keywords:** SMIL, XML, XSLT, multimedia, transformations

11 Frameworks for component-based client/server computing



Scott M. Lewandowski

March 1998 **Computing Surveys (CSUR)** , Volume 30 Issue 1

**Publisher:** ACM [Request Permissions](#)

Full text available: Pdf (243.81 KB) Additional Information: [full citation](#), [references](#), [cited by](#), [index terms](#)

**Bibliometrics:** Downloads (6 Weeks): 18, Downloads (12 Months): 233, Citation Count: 18

12 Automatically generating OLAP schemata from conceptual graphical models



Karl Hahn, Carsten Sapla, Markus Blaschka

November **DOLAP '00: Proceedings of the 3rd ACM international workshop on Data warehousing and OLAP**

**Publisher:** ACM [Request Permissions](#)

Full text available: Pdf (118.84 KB) Additional Information: [full citation](#), [references](#), [cited by](#), [index terms](#)

**Bibliometrics:** Downloads (6 Weeks): 7, Downloads (12 Months): 89, Citation Count: 7

**Keywords:** OLAP, conceptual design, data warehouse, graphical multidimensional modeling notation, multidimensional schema generation

### 13 [Automatic data layout for distributed-memory machines](#)



[Ken Kennedy, Ulrich Kremer](#)

July **Transactions on Programming Languages and Systems (TOPLAS)** ,  
1998 Volume 20 Issue 4

**Publisher:** ACM [Request Permissions](#)

Full text available: Pdf (633.20 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [cited by](#),  
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**Bibliometrics:** Downloads (6 Weeks): 7, Downloads (12 Months): 59, Citation Count: 25

The goal of languages like Fortran D or High Performance Fortran (HPF) is to provide a simple yet efficient machine-independent parallel programming model. After the algorithm selection, the data layout choice is the key intellectual challenge in writing ...

**Keywords:** high performance Fortran

### 14 [A brief history of just-in-time](#)



[John Aycock](#)

June 2003 **Computing Surveys (CSUR)** , Volume 35 Issue 2

**Publisher:** ACM [Request Permissions](#)

Full text available: Pdf (171.09 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [cited by](#),  
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**Bibliometrics:** Downloads (6 Weeks): 80, Downloads (12 Months): 716, Citation Count: 24

Software systems have been using "just-in-time" compilation (JIT) techniques since the 1960s. Broadly, JIT compilation includes any translation performed dynamically, after a program has started execution. We examine the motivation behind JIT compilation ...

**Keywords:** Just-in-time compilation, dynamic compilation

### 15 [Compiler-based I/O prefetching for out-of-core applications](#)



[Angela Demke Brown, Todd C. Mowry, Orran Krieger](#)

May **Transactions on Computer Systems (TOCS)** , Volume 19 Issue 2  
2001

**Publisher:** ACM [Request Permissions](#)

Full text available: Pdf (499.03 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [cited by](#),  
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**Bibliometrics:** Downloads (6 Weeks): 7, Downloads (12 Months): 67, Citation Count: 16

Current operating systems offer poor performance when a numeric application's working set does not fit in main memory. As a result, programmers who wish to solve "out-of-core" problems efficiently are typically faced with the onerous task ...

**Keywords:** compiler optimization, prefetching, virtual memory


**16** Techniques for obtaining high performance in Java programs



Iffat H. Kazi, Howard H. Chen, Berdenia Stanley, David J. Lillia

September 2000 **Computing Surveys (CSUR)** , Volume 32 Issue 3

**Publisher:** ACM  [Request Permissions](#)

Full text available:  Pdf (816.13 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [cited by](#), [index terms](#)

**Bibliometrics:** Downloads (6 Weeks): 86, Downloads (12 Months): 489, Citation Count: 7

This survey describes research directions in techniques to improve the performance of programs written in the Java programming language. The standard technique for Java execution is interpretation, which provides for extensive portability of programs. ...

**Keywords:** Java, Java virtual machine, bytecode-to-source translators, direct compilers, dynamic compilation, interpreters, just-in-time compilers


**17** Creating and preserving locality of java applications at allocation and garbage collection times



Yefim Shuf, Manish Gupta, Hubertus Franke, Andrew Appel, Jaswinder Pal Singh

November 2002 **OOPSLA '02: Proceedings of the 17th ACM SIGPLAN conference on Object-oriented programming, systems, languages, and applications**

**Publisher:** ACM  [Request Permissions](#)

Full text available:  Pdf (180.20 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [cited by](#), [index terms](#)

**Bibliometrics:** Downloads (6 Weeks): 5, Downloads (12 Months): 61, Citation Count: 11

The growing gap between processor and memory speeds is motivating the need for optimization strategies that improve data locality. A major challenge is to devise techniques suitable for pointer-intensive applications. This paper presents two techniques ...

**Keywords:** JVM, Java, garbage collection, heap traversal, locality, locality based graph traversal, memory allocation, memory management, object co-allocation, object placement, prolific types, run-time systems

Also published in:


November 2002 **SIGPLAN Notices** Volume 37 Issue 11

**18** Anatomy of a native XML base management system

T. Fiebig, S. Helmer, C.-C. Kanne, G. Moerkotte, J. Neumann, R. Schiele, T. Westmann

December 2002 **The VLDB Journal — The International Journal on Very Large Data Bases** , Volume 11 Issue 4

**Publisher:** Springer-Verlag New York, Inc.

Full text available:  Pdf (300.97 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [cited by](#), [index terms](#)

**Bibliometrics:** Downloads (6 Weeks): 14, Downloads (12 Months): 119, Citation Count: 32

Several alternatives to manage large XML document collections exist, ranging from file systems over relational or other database systems to specifically tailored XML base management systems. In this paper we give a tour of Natix, a database management ...


**Keywords:** Database, XML

**19** Composable ad hoc location-based services for heterogeneous mobile clients

Todd D. Hodes, Randy H. Katz

October **Wireless Networks** , Volume 5 Issue 5  
1999

**Publisher:** Kluwer Academic Publishers

Full text available:  Pdf (403.18 KB) Additional Information: [full citation](#), [references](#), [cited by](#), [index terms](#)

**Bibliometrics:** Downloads (6 Weeks): 12, Downloads (12 Months): 81, Citation Count: 9


**20** Data and memory optimization techniques for embedded systems



P. R. Panda, F. Catthoor, N. D. Dutt, K. Danckaert, E. Brockmeyer, C. Kulkarni, A. Vandercappelle, P. G. Keldsberg

April **Transactions on Design Automation of Electronic Systems (TODAES)** , Volume 6 Issue 2  
2001

**Publisher:** ACM  [Request Permissions](#)

Full text available:  Pdf (339.91 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [cited by](#), [index terms](#)

**Bibliometrics:** Downloads (6 Weeks): 91, Downloads (12 Months): 583, Citation Count: 80





We present a survey of the state-of-the-art techniques used in performing data and memory-related optimizations in embedded systems. The optimizations are targeted directly or indirectly at the memory subsystem, and impact one or more out of three important ...

**Keywords:** DRAM, SRAM, address generation, allocation, architecture exploration, code transformation, data cache, data optimization, high-level synthesis, memory architecture customization, memory power dissipation, register file, size estimation, survey

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